

**GOA COLLEGE OF ENGINEERING**

FARMAGUDI, PONDA, GOA - 403401

15/05/2019 To 10/06/2019

**REGULAR EXAMINATION RESULT OF SEMESTER - III (ELECTRONICS & TELECOMMUNICATION) EXAMINATION HELD IN MAY-JUNE 2019 (REVISED COURSE)**

Seat No	A.R.E	Male	Name of Candidate	Sessional	Term Work	Theory	Theory	Total	Total	Practical	Practical	Oral	Passes	Total	Total	Remarks	PR Number
Roll No	Code	Female		Marks	Marks								Fails	Sem-3			
Category				50/25	50/25	Obtd.	Grace	Obtd.	Grace	Obtd.	Grace				Grace		
<b>197110</b>	<b>R</b>	<b>F</b>	<b>NAIK PRAJAKTA PUNDALIK</b>														<b>201610596</b>
<b>161104039</b>																	
<b>OBC</b>			Applied Mathematics - III	15		40		55					P				
			Economics and Management	11		40		51					+				
			Algorithms for Data Structures	12	24	41		77					+				
			Electronic Devices and Circuits – I	8		36	\$4	44	\$6	15			+				
			Digital System Design	7		40		47	\$3	16			+				
			Electrical Circuits and Systems	12		50		62				19	+				
							<b>Sem-2 PASS</b>		<b>Sem-1 PASS</b>					<b>*386</b>	<b>\$9</b>	<b>PASSES *</b>	
<b>197111</b>	<b>R</b>	<b>M</b>	<b>GAUNEKAR SAISH BABULI</b>														<b>201704568</b>
<b>171104020</b>																	
<b>ST</b>			Applied Mathematics - III	10		35	\$5	45	\$5				+				
			Economics and Management	3		A		3					F				
			Algorithms for Data Structures	0	11	71		82					P				
			Electronic Devices and Circuits – I	6		A		6		13			F				
			Digital System Design	2		74		76		11			P				
			Electrical Circuits and Systems	10		41		51				12	+				
							<b>Sem-2 PASS</b>		<b>Sem-1 PASS</b>					<b>299</b>	<b>\$5</b>	<b>FAILS</b>	
<b>197112</b>	<b>R</b>	<b>M</b>	<b>GAUNS KURTIKAR ASHUTOSH UMESH</b>														<b>201704576</b>
<b>171104021</b>																	
<b>ST</b>			Applied Mathematics - III	13		40		53					+				
			Economics and Management	10		40		50					+				
			Algorithms for Data Structures	11	12	49		72					+				
			Electronic Devices and Circuits – I	7		23		30		13			F				
			Digital System Design	5		42		47	\$3	11			P				
			Electrical Circuits and Systems	10		35	\$5	45	\$5			12	+				
							<b>Sem-2 PASS</b>		<b>Sem-1 PASS</b>					<b>333</b>	<b>\$8</b>	<b>FAILS</b>	
<b>197113</b>	<b>R</b>	<b>M</b>	<b>KAWLEKAR MITANSHU PRAKASH</b>														<b>201705122</b>
<b>171104030</b>																	
<b>SC</b>			Applied Mathematics - III	10		40		50					+				
			Economics and Management	10		52		62					+				
			Algorithms for Data Structures	9	18	62		89					+				
			Electronic Devices and Circuits – I	7		46		53		15			P				
			Digital System Design	4		25		29		12			F				
			Electrical Circuits and Systems	10		38	\$2	48	\$2			17	+				
							<b>Sem-2 FAIL</b>		<b>Sem-1 PASS</b>					<b>375</b>	<b>\$2</b>	<b>FAILS</b>	
<b>197114</b>	<b>R</b>	<b>M</b>	<b>KORGAONKAR BRIJESH BUDHAJI</b>														<b>201704588</b>
<b>171104032</b>																	
<b>OBC</b>			Applied Mathematics - III	10		15		25					F				
			Economics and Management	11		40		51					P				
			Algorithms for Data Structures	5	13	NA		18					F				
			Electronic Devices and Circuits – I	10		NA		10		21			F				
			Digital System Design	5		17		22		10			F				
			Electrical Circuits and Systems	10		35	\$5	45	\$5			19	+				
							<b>Sem-2 FAIL</b>		<b>Sem-1 FAIL</b>					<b>221</b>	<b>\$5</b>	<b>FAILS</b>	
<b>197115</b>	<b>R</b>	<b>M</b>	<b>KUVELKAR SHANTAN AS VAIBHAV R</b>														<b>201711500</b>
<b>171104034</b>																	
<b>GENERAL</b>			Applied Mathematics - III	10		42		52					+				
			Economics and Management	10		44		54					+				
			Algorithms for Data Structures	8	10	45		63					+				
			Electronic Devices and Circuits – I	3		46		49	\$1	19			+				
			Digital System Design	7		52		59		14			P				
			Electrical Circuits and Systems	10		49		59				16	+				
							<b>Sem-2 PASS</b>		<b>Sem-1 PASS</b>					<b>*385</b>	<b>\$1</b>	<b>PASSES *</b>	

#: ncc/nss/sport; \$ grace; a: absent; e: exemption; p: passes; f: fails; -+ marks carried over , \* passed in compartment  
discrepancies if any should be brought to the notice of academic incharge immediately

entered by

read by

checked by

principal

Page 1 of 3

**REGULAR EXAMINATION RESULT OF SEMESTER - III (ELECTRONICS & TELECOMMUNICATION) EXAMINATION HELD IN MAY-JUNE 2019 (REVISED COURSE)**

Seat No	A.R.E	Male	Name of Candidate	Sessional	Term Work	Theory	Theory	Total	Total	Practical	Practical	Oral	Passes	Total	Total	Remarks	PR Number
Roll No	Code	Female		Marks	Marks								Fails	Sem-3			
Category				50/25	50/25	Obtd.	Grace	Obtd.	Grace	Obtd.	Grace				Grace		
<b>197116</b>	<b>R</b>	<b>F</b>	<b>MAULANKAR DEEPIKA DILIP</b>														<b>201705096</b>
<b>171104037</b>																	
<b>OBC</b>			Applied Mathematics - III	18		50		68					+				
			Economics and Management	13		A		13					F				
			Algorithms for Data Structures	10	13	57		80					+				
			Electronic Devices and Circuits – I	11		42		53		22			+				
			Digital System Design	11		40		51		10			+				
			Electrical Circuits and Systems	7		54		61				10	+				
							<b>Sem-2 FAIL</b>		<b>Sem-1 FAIL</b>					<b>368</b>		<b>FAILS</b>	
<b>197117</b>	<b>R</b>	<b>F</b>	<b>VELIP DHANASHREE SADANAND</b>														<b>201704578</b>
<b>171104063</b>																	
<b>ST</b>			Applied Mathematics - III	10		18		28					F				
			Economics and Management	11		40		51					+				
			Algorithms for Data Structures	6	11	67		84					+				
			Electronic Devices and Circuits – I	10		47		57		14			+				
			Digital System Design	7		44		51		10			+				
			Electrical Circuits and Systems	13		35	\$5	48	\$5			10	+				
							<b>Sem-2 FAIL</b>		<b>Sem-1 PASS</b>					<b>353</b>	<b>\$5</b>	<b>FAILS</b>	
<b>197118</b>	<b>R</b>	<b>M</b>	<b>TILVE SARVAJIT SUDESH</b>														<b>201704926</b>
<b>171104069</b>																	
<b>GENERAL</b>			Applied Mathematics - III	10		43		53					+				
			Economics and Management	12		62		74					+				
			Algorithms for Data Structures	13	20	75		108					+				
			Electronic Devices and Circuits – I	7		50		57		14			P				
			Digital System Design	10		41		51		22			+				
			Electrical Circuits and Systems	10		50		60				21	+				
							<b>Sem-2 PASS</b>		<b>Sem-1 PASS</b>					<b>*460</b>		<b>PASSES *</b>	
<b>197119</b>	<b>R</b>	<b>F</b>	<b>AKALWADI ASHVINI SATYPPA</b>														<b>201704559</b>
<b>171204001</b>																	
<b>GENERAL</b>			Applied Mathematics - III	14		35	\$5	49	\$5				P				
			Economics and Management	12		40		52					+				
			Algorithms for Data Structures	11	24	47		82					+				
			Electronic Devices and Circuits – I	13		40		53		18			+				
			Digital System Design	11		40		51		18			+				
			Electrical Circuits and Systems	10		56		66				16	+				
							<b>Sem-2 -</b>		<b>Sem-1 -</b>					<b>*405</b>	<b>\$5</b>	<b>PASSES *</b>	
<b>197120</b>	<b>R</b>	<b>F</b>	<b>DYAPUR SHILPA SHIVANAND</b>														<b>201705120</b>
<b>171204005</b>																	
<b>GENERAL</b>			Applied Mathematics - III	11		29		40					F				
			Economics and Management	10		40		50					+				
			Algorithms for Data Structures	10	24	48		82					+				
			Electronic Devices and Circuits – I	10		38	\$2	48	\$2	16			+				
			Digital System Design	6		40		46	\$4	24			+				
			Electrical Circuits and Systems	10		41		51				14	+				
							<b>Sem-2 -</b>		<b>Sem-1 -</b>					<b>371</b>	<b>\$6</b>	<b>FAILS</b>	
<b>197121</b>	<b>R</b>	<b>M</b>	<b>SAWANT ANIL APPA</b>														<b>201704564</b>
<b>171204010</b>																	
<b>GENERAL</b>			Applied Mathematics - III	10		14		24					F				
			Economics and Management	10		40		50					+				
			Algorithms for Data Structures	11	23	40		74					+				
			Electronic Devices and Circuits – I	10		36	\$4	46	\$4	15			+				
			Digital System Design	4		40		44	\$6	18			+				
			Electrical Circuits and Systems	10		40		50				13	+				
							<b>Sem-2 -</b>		<b>Sem-1 -</b>					<b>334</b>	<b>\$10</b>	<b>FAILS</b>	

REGULAR EXAMINATION RESULT OF SEMESTER - III (ELECTRONICS & TELECOMMUNICATION) EXAMINATION HELD IN MAY-JUNE 2019 (REVISED COURSE)

Seat No	A.R.E	Male	Name of Candidate	Sessional	Term Work	Theory	Theory	Total	Total	Practical	Practical	Oral	Passes	Total	Total	Remarks	PR Number
Roll No	Code	Female		Marks	Marks								Fails	Sem-3			
Category				50/25	50/25	Obtd.	Grace	Obtd.	Grace	Obtd.	Grace				Grace		
197122	R	M	GOVEKAR SUBHASHISH KETAN														201811779
181204003																	
GENERAL			Applied Mathematics - III	10		35	\$5	45	\$5				+				
			Economics and Management	12		47		59					+				
			Algorithms for Data Structures	9	20	48		77					+				
			Electronic Devices and Circuits – I	9		35	\$5	44	\$6	21			P				
			Digital System Design	3		51		54		15			P				
			Electrical Circuits and Systems	10		44		54				10	P				
							Sem-2 -		Sem-1 -					*379	\$11	PASSES *	
197123	R	M	KAVTHANKER PRASAD SADASHIV														201807710
181204004																	
OBC			Applied Mathematics - III	5		NA		5					F				
			Economics and Management	10		35	\$5	45	\$5				P				
			Algorithms for Data Structures	4	15	41		60					+				
			Electronic Devices and Circuits – I	11		36	\$4	47	\$4	20			P				
			Digital System Design	5		18		23		15			F				
			Electrical Circuits and Systems	8		NA		8				14	F				
							Sem-2 -		Sem-1 -					237	\$9	FAILS	
197124	R	M	KHANOLKAR VISHVESH UDAY														201811777
181204005																	
GENERAL			Applied Mathematics - III	14		46		60					+				
			Economics and Management	14		47		61					+				
			Algorithms for Data Structures	12	24	59		95					+				
			Electronic Devices and Circuits – I	6		A		6		19			F				
			Digital System Design	12		40		52		24			+				
			Electrical Circuits and Systems	16		36	\$4	52	\$4			19	+				
							Sem-2 -		Sem-1 -					388	\$4	FAILS	
197125	R	M	SHARMA UPESH														201811774
181204010																	
GENERAL			Applied Mathematics - III	10		NA		10					F				
			Economics and Management	12		49		61					P				
			Algorithms for Data Structures	9	15	50		74					+				
			Electronic Devices and Circuits – I	3		NA		3		16			F				
			Digital System Design	10		46		56		18			P				
			Electrical Circuits and Systems	8		51		59				13	P				
							Sem-2 -		Sem-1 -					310		FAILS	

Declared on : \_\_\_\_\_

Entered by : \_\_\_\_\_

Read by : \_\_\_\_\_

Checked by : \_\_\_\_\_

Principal